

## VPX3-ZU1-SDR Products Brief

*3U OpenVPX Solutions for SDR/EW with ADI's RadioVerse RF Transceivers*

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# Overview

**PanaTeQ's VPX3-ZU1-SDR** are 3U OpenVPX modules based on the Zynq UltraScale+ MPSoC device from Xilinx coupled to RadioVerse Analog Devices RF Wideband Transceivers AD9371, AD9375 or ADRV9009 for a broad range of applications such as Software Defined Radio, MILCOM, massive MIMO, Phase Array Radar and Electronic Warfare.

PanaTeQ provides solutions for **Ruggedized Air-Cooled** and **Conduction Cooled** systems.

These VPX3-ZU1-SDR modules are based on the following PanaTeQ's sub-modules (boards):

VPX3-ZU1, FMC-ZU1RF-A/B, FMC-ZU2RF-A/B, XMC-SDR-A and VPX3-XMC-SDR-A

The **VPX3-ZU1** is a 3U OpenVPX module based on a Xilinx Zynq Ultrascale+ MPSoC with a FMC 57.1 site, HW/SW compatible with ZCU102 Evaluation board from Xilinx.

The **FMC-ZU1RF-A** is a FMC based on an Analog Devices AD9371, HW/SW compatible with ADRV9371 Evaluation Board from Analog Devices.

The **FMC-ZU1RF-B** is a FMC based on an Analog Devices AD9375, HW/SW compatible with ADRV9371 Evaluation board from Analog Devices.

The **FMC-ZU2RF-A** is a FMC based on an Analog Devices ADRV9009, HW/SW compatible with ADRV9009 Evaluation board from Analog Devices.

The **FMC-ZU2RF-B** is a FMC based on two Analog Devices ADRV9009.

The **XMC-SDR-A** is a XMC based on two Analog Devices ADRV9009, HW/SW compatible with ADRV9009-11EG RF SOM from Analog Devices.

The **VPX3-XMC-SDR-A** is a optimized 3U OpenVPX carrier module for the **XMC-SDR-A**.

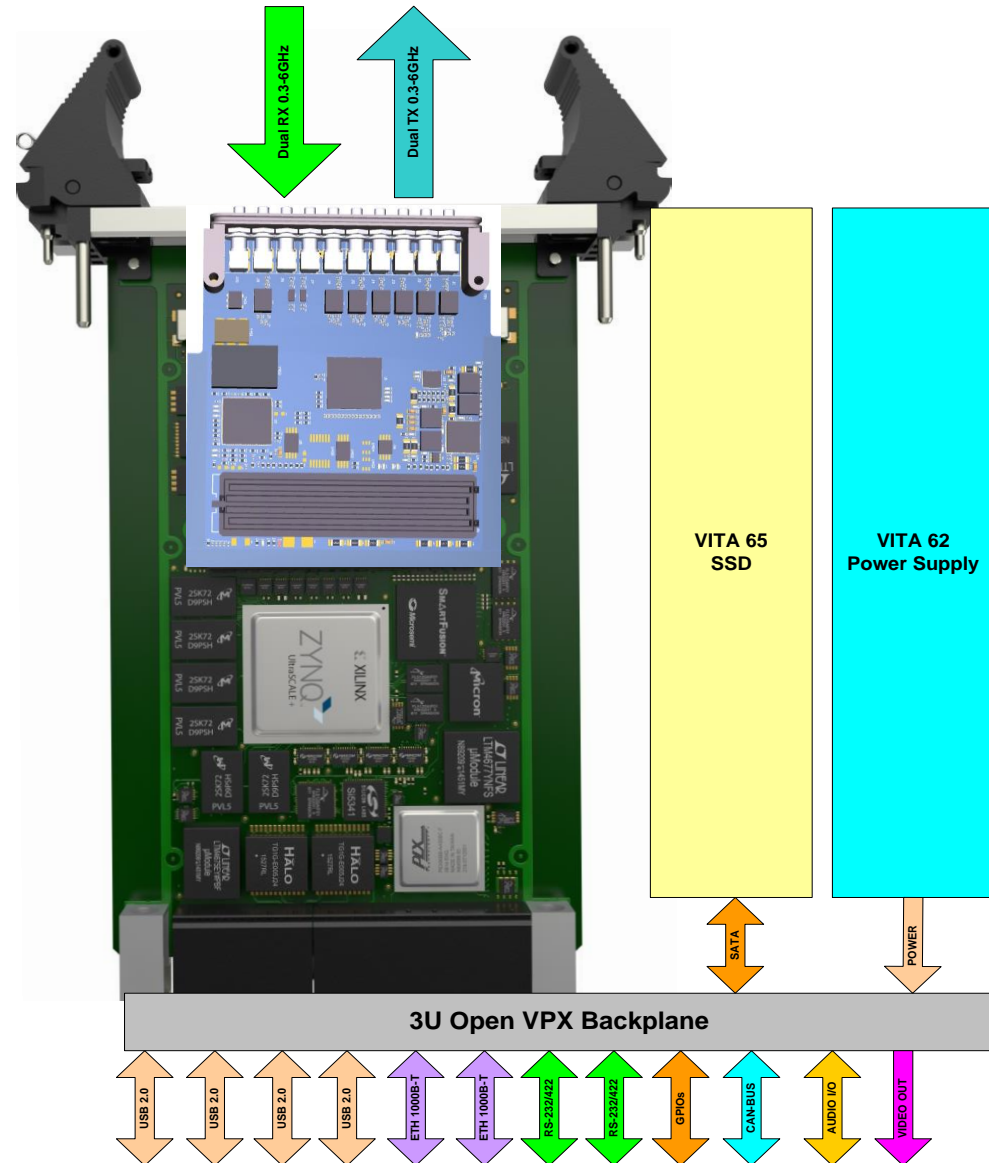
# Typical Application : Software Defined Radio System (SWaP-C)

## VPX3-ZU1-SDR-A/B/C/D

Air Cooled



Conduction Cooled



# Analog Devices RF Transceivers Comparison

Part #	RF Tuning Range	Bandwidth	Channels	Interface	Digital Pre Distorsion (DPD)	Hop Time	IQ Rate
AD9361	70MHz – 6GHz	56MHz Rx & Tx	2Rx, 2Tx	JESD207 CMOS/LVDS	N/A	250us	50MSPS
AD9371	300MHz – 6GHz	100 MHz Rx 250MHz Tx	2Rx, 2Tx, 2ORx, 3SnRx	6Gbps JESD204B	N/A	1ms	125MSPS
AD9375	300MHz – 6GHz	100 MHz Rx 250MHz Tx	2Rx, 2Tx, 2ORx, 3SnRx	6Gbps JESD204B	Linearization BW up to 40MHz	1ms	125MSPS
ADRV9009	75MHz – 6GHz	200MHz Rx, 450MHz Tx & ORx	2Rx, 2Tx & 2ORx	12Gbps JESD204B	N/A	70us	250MSPS

# PanaTeQ 3U VPX SDR Products Table

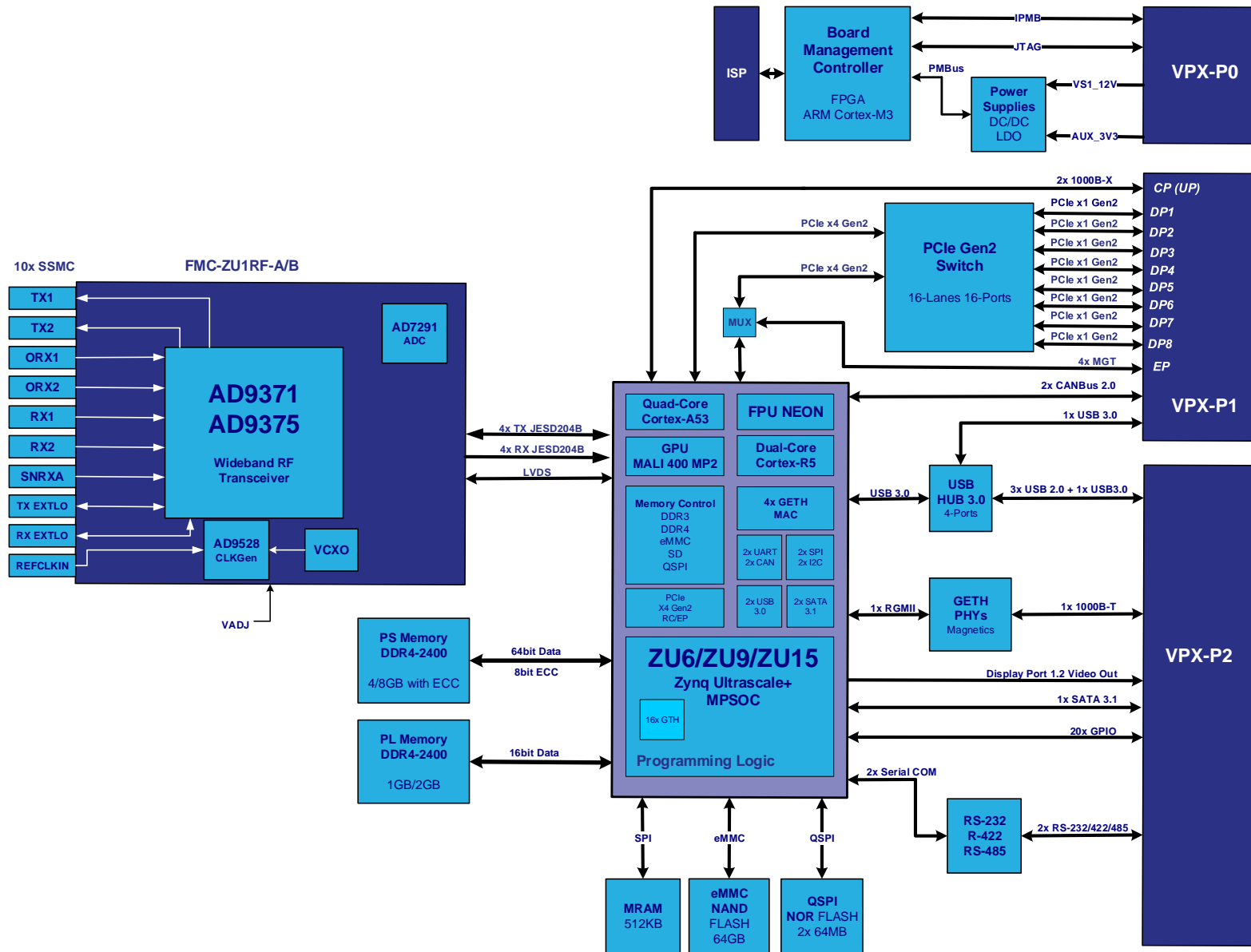
Product Name	Zynq MPSOC	RF Transceiver	MIMO	ADI Eval Board	Availability
VPX3-ZU1-SDR-A	ZU6/ZU9/ZU15	AD9371	2x2	ADRV9371-W	Now
VPX3-ZU1-SDR-B	ZU6/ZU9/ZU15	AD9375	2x2	ADRV9375-W	Now
VPX3-ZU1-SDR-C	ZU6/ZU9/ZU15	ADRV9009	2x2	ADRV9009-W	Contact Us
VPX3-ZU1-SDR-D	ZU6/ZU9/ZU15	2x ADRV9009	4x4	ADRV9009-11EG	Contact Us
VPX3-XMC-SDR-A	ZU7/ZU11	2x ADRV9009	4x4	ADRV9009-11EG	Contact Us

# VPX3-ZU1-SDR-A/B Features

## Technical Specifications

- 3U VPX (**VITA 46.0**) and OpenVPX (**VITA 65**) specifications compliant
- Compatibility with ADI Eval boards **ADRV9371** (-A model), **ADRV9375** (-B model)
- **AD9371** (-A model), **AD9375** (-B model) RF Wideband Transceivers
- Two TX and Two RX coherent channels synchronized in frequency and phase
- Wide tuning range 300MHz to 6GHz
- Max receiver BW 100MHz. Up to 200MHz using 2 receivers
- Max transmitter synthesis BW 250MHz
- On-board Xilinx **Zynq UltraScale+ MPSoC** as Baseband processor
- Compatibility with Xilinx Eval Board **ZCU102**
- **ZU6/ZU9/ZU15** FFVC-900 Package
- Up to 8GB DDR4-2400 64-bit Processing System (PS) memory with 8-bit ECC
- Up to 2GB DDR4-2400 16-bit Programmable Logic (PL) memory
- eMMC 64GB (V4.51), MRAM 512KB
- On-board **PCIe Gen2 Switch** PEX8619, 16-Ports 16-Lanes. No SBC required in the VPX System
- 1x 1000Base-T on VPX-P2 and 2x 1000Base-X/SGMII on VPX-P1
- 2x RS-232/422/485, 2x CAN 2.0B and 20x GPIOs (LVCMOS 2.5V or LVDS 2.5V) on VPX-P2
- 2x USB 3.0/2.0, 2x USB 2.0, 1x SATA 3.1 and DisplayPort 1.2 Video Out on VPX-P2
- Advanced PanaTeQ Board Management Controller (BMC) ARM M3 based and Power Management Bus (PMBus)
- Industrial Air Cooled and Conduction Cooled (**VITA 48.0**) versions available

# VPX3-ZU1-SDR-A/B Block Diagram



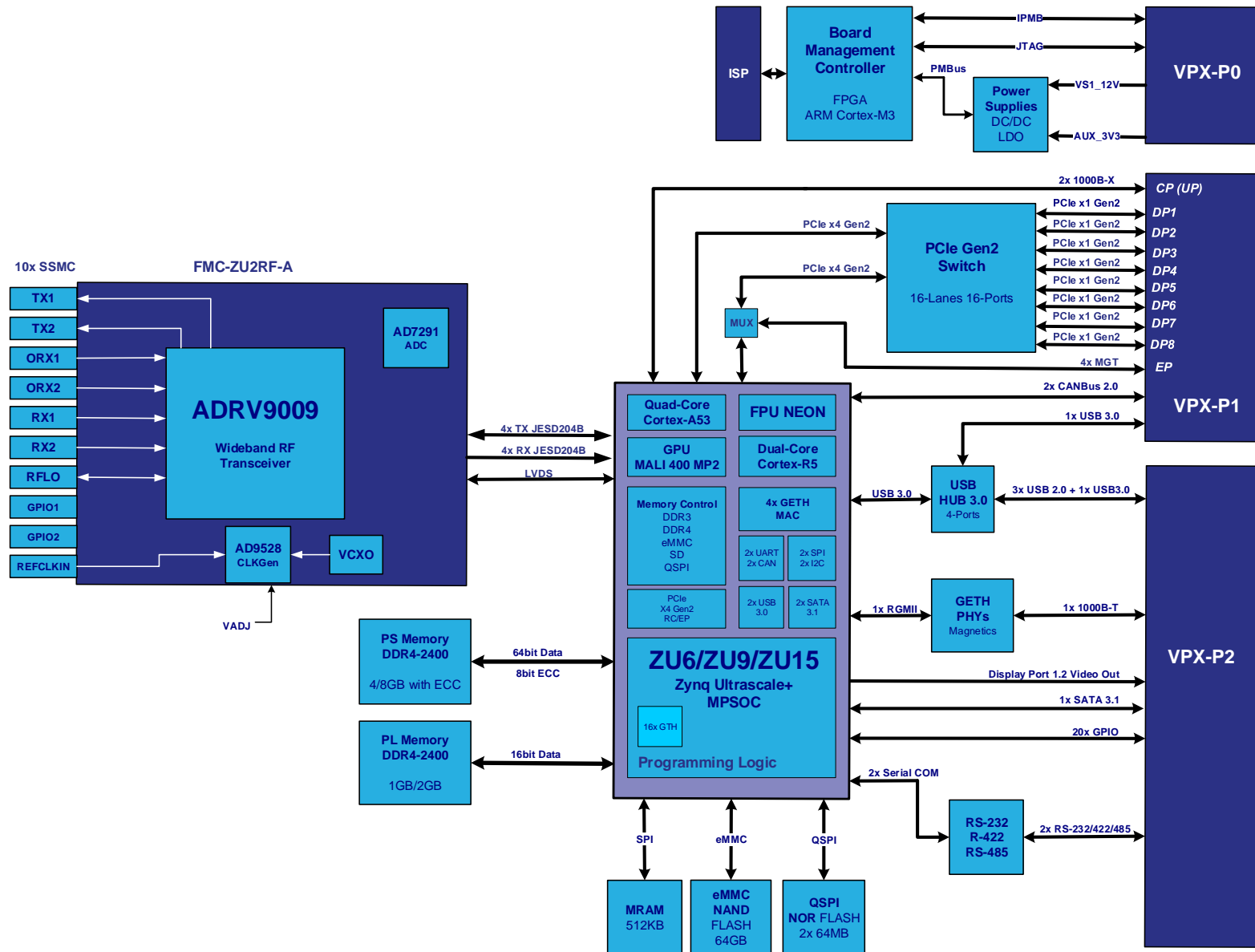


# VPX3-ZU1-SDR-C Module Features

## Technical Specifications

- 3U VPX (**VITA 46.0**) and OpenVPX (**VITA 65**) specifications compliant
- Compatibility with ADI Eval boards **ADRV9009**
- **ADRV9009** RF Wideband Transceiver
- Two TX and Two RX coherent channels synchronized in frequency and phase
- Wide tuning range 75MHz to 6GHz
- Max receiver BW 200MHz. Up to 400MHz using 2 receivers
- Max transmitter synthesis BW 450MHz
- On-board Xilinx **Zynq UltraScale+ MPSoC** as Baseband processor
- Compatibility with Xilinx Eval Board **ZCU102**
- **ZU6/ZU9/ZU15** FFVC-900 Package
- Up to 8GB DDR4-2400 64-bit Processing System (PS) memory with 8-bit ECC
- Up to 2GB DDR4-2400 16-bit Programmable Logic (PL) memory
- eMMC 64GB (V4.51), MRAM 512KB
- On-board **PCIe Gen2 Switch** PEX8619, 16-Ports 16-Lanes. No SBC required in the VPX System
- 1x 1000Base-T on VPX-P2 and 2x 1000Base-X/SGMII on VPX-P1
- 2x RS-232/422/485, 2x CAN 2.0B and 20x GPIOs (LVCMOS 2.5V or LVDS 2.5V) on VPX-P2
- 2x USB 3.0/2.0, 2x USB 2.0, 1x SATA 3.1 and DisplayPort 1.2 Video Out on VPX-P2
- Advanced PanaTeQ Board Management Controller (BMC) ARM M3 based and Power Management Bus (PMBus)
- Industrial Air Cooled and Conduction Cooled (**VITA 48.0**) versions available

# VPX3-ZU1-SDR-C Block Diagram

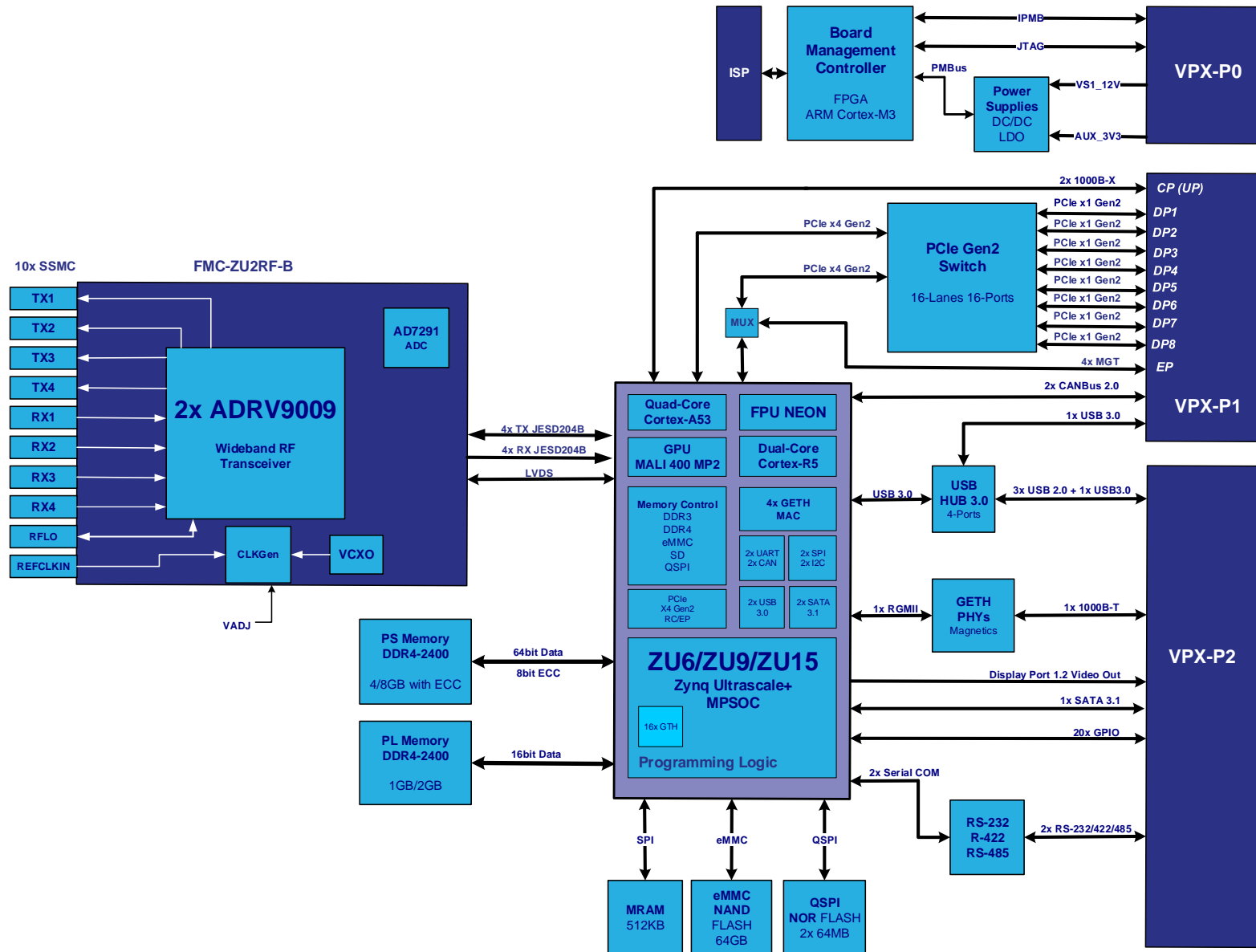


# VPX3-ZU1-SDR-D Module Features (Q1 2019)

## Technical Specifications

- 3U VPX (**VITA 46.0**) and OpenVPX (**VITA 65**) specifications compliant
- Compatibility with ADI Eval boards **ADRV9009** (single Transceiver)
- Dual **ADRV9009** RF Wideband Transceiver
- Four TX and Four RX coherent channels synchronized in frequency and phase
- Wide tuning range 75MHz to 6GHz
- Max receiver BW 200MHz. Up to 800MHz using 4 receivers
- Max transmitter synthesis BW 450MHz
- On-board Xilinx **Zynq UltraScale+ MPSoC** as Baseband processor
- Compatibility with Xilinx Eval Board **ZCU102**
- **ZU6/ZU9/ZU15** FFVC-900 Package
- Up to 8GB DDR4-2400 64-bit Processing System (PS) memory with 8-bit ECC
- Up to 2GB DDR4-2400 16-bit Programmable Logic (PL) memory
- eMMC 64GB (V4.51), MRAM 512KB
- On-board **PCIe Gen2 Switch** PEX8619, 16-Ports 16-Lanes. No SBC required in the VPX System
- 1x 1000Base-T on VPX-P2 and 2x 1000Base-X/SGMII on VPX-P1
- 2x RS-232/422/485, 2x CAN 2.0B and 20x GPIOs (LVCMOS 2.5V or LVDS 2.5V) on VPX-P2
- 2x USB 3.0/2.0, 2x USB 2.0, 1x SATA 3.1 and DisplayPort 1.2 Video Out on VPX-P2
- Advanced PanaTeQ Board Management Controller (BMC) ARM M3 based and Power Management Bus (PMBus)
- Industrial Air Cooled and Conduction Cooled (**VITA 48.0**) versions available

# VPX3-ZU1-SDR-D Block Diagram

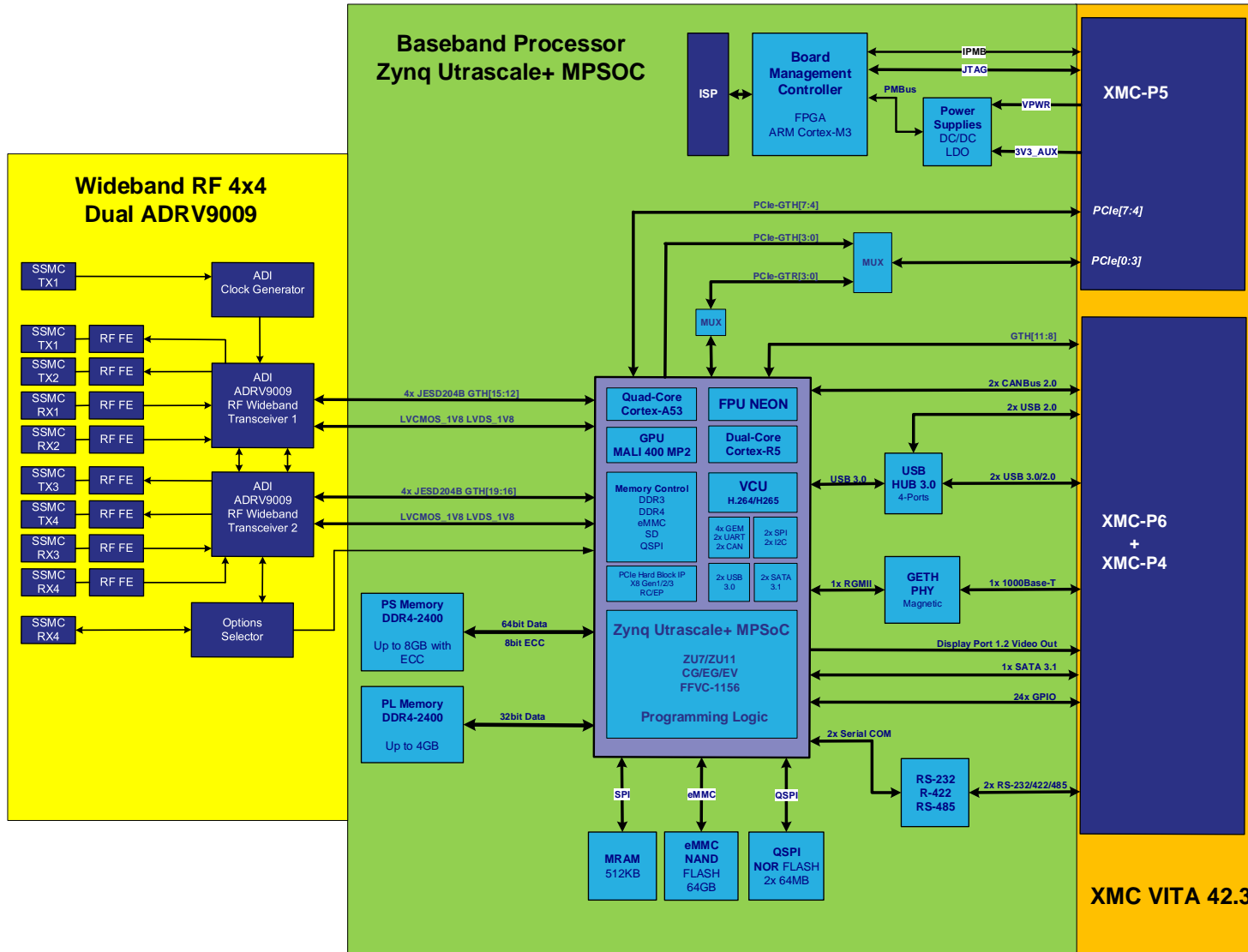


# VPX3-XMC-SDR-A Module Features (Q2 2019)

## Technical Specifications

- 3U VPX (**VITA 46.0**) and OpenVPX (**VITA 65**) specifications compliant
- Compatibility with ADI Eval boards **ADRV9009-11EG**
- Dual **ADRV9009** RF Wideband Transceiver
- Four TX and Four RX coherent channels synchronized in frequency and phase
- Wide tuning range 75MHz to 6GHz
- Max receiver BW 200MHz. Up to 800MHz using 4 receivers
- Max transmitter synthesis BW 450MHz
- On-board Xilinx **Zynq UltraScale+ MPSoC** as Baseband processor
- **ZU7/ZU11/** FFVC-1156 Package
- Up to 8GB DDR4-2400 64-bit Processing System (PS) memory with 8-bit ECC
- Up to 4GB DDR4-2400 32-bit Programmable Logic (PL) memory
- eMMC 64GB (V4.51), MRAM 512KB
- On-board **PCIe Gen3 Switch**. No SBC required in the VPX System
- 1x 1000Base-T on VPX-P2 and 2x 1000Base-X/SGMII on VPX-P1
- 2x RS-232/422/485, Optional 10GE Optical Interface (VITA 66.4) on VPX-P2
- 2x USB 3.0/2.0, 2x USB 2.0, 1x SATA 3.1 and DisplayPort 1.2 Video Out on VPX-P2
- Advanced PanaTeQ Board Management Controller (BMC) ARM M3 based and Power Management Bus (PMBus)
- Industrial Air Cooled and Conduction Cooled (**VITA 48.0**) versions available

# XMC-SDR-A Block Diagram



# Estimated Power Budget (VPX 12V)

## Estimated Worst Case Power Budget

- Xilinx Zynq UltraScale+ ZU9G = 10-30 W
- 6 x 4 Gbit DDR4-2400 (2 GB + 512MB) = 4W
- PCIe Switch = 3W
- Miscellaneous = 4W
- DC/DC = 4W
- FMC RF = 10W

## Summary

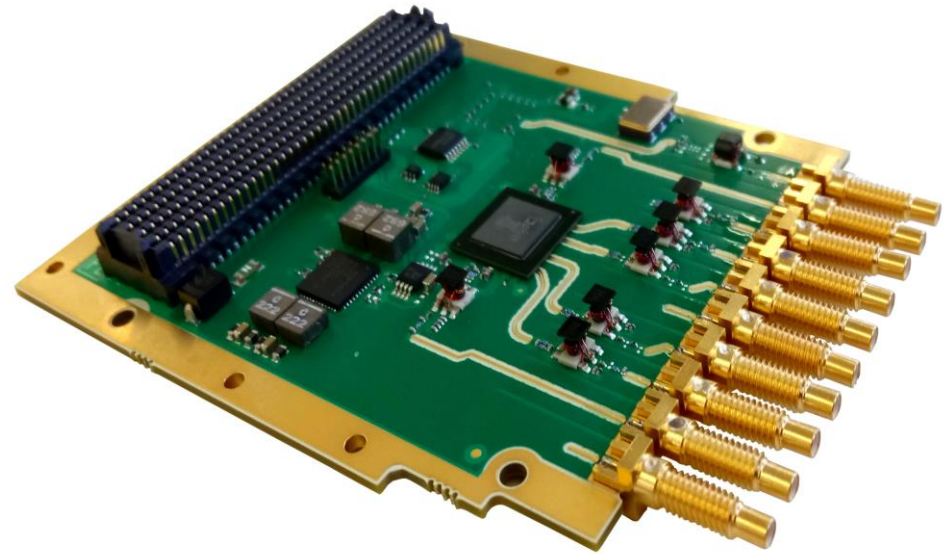
- Low Power configuration = 35 W
- Standard configuration = 45W
- High-performance configuration = 55W

# FMC-ZU1RF-A/B-W

## FMC Wideband RF Transceiver AD9371/AD9375 based

### Technical Specifications

- Vita 57.1-2010 specification compliant
- FMC High Pin Connector (HPC)
- Analog Devices AD9371 (-A) or AD9375 (-B) RF Transceiver
- JESD024B interface up to 6144 Mbps (4x TX, 4x RX)
- LA Bus LVDA and Single-Ended
- Operates with  $V_{Adj} = 2.5V$  to  $1.8V$
- ADRV9371 (-A) ADRV9375 (-B) ADI eval boards HW/SW
- Dual Receivers (Rx)
- Observation Receiver (ORx) with two input
- Sniffer Receiver (SnRx) with 1 input
- TX Ext LO Input/Output. RX Ext LO Input/Output
- Reference Clock Input
- RF Coverage 300MHz to 6.0 GHz. Tx Synthesis Bandwidth (BW) to 250MHz. Rx Bandwidth: 7MHz to 100MHz
- Industrial Air Cooled and Conduction Cooled versions



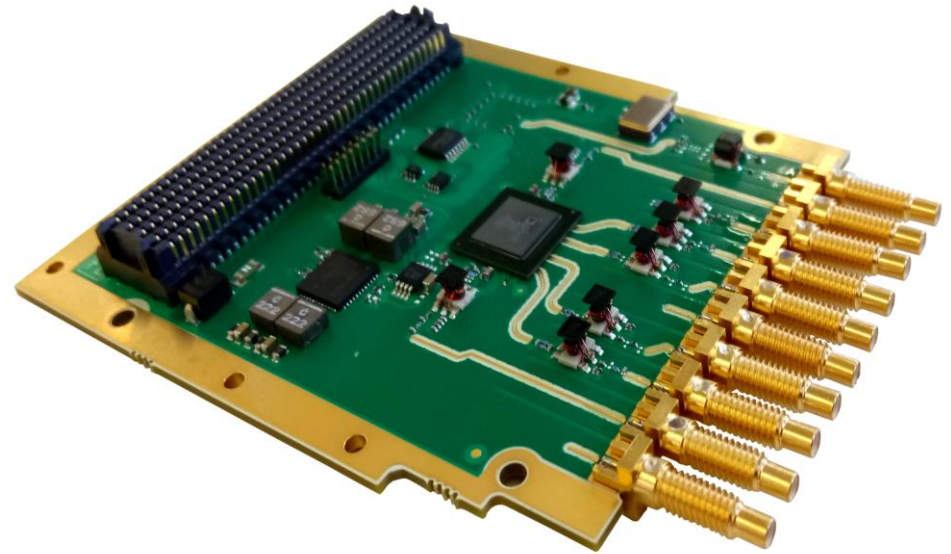


# FMC-ZU2RF-A-W

## FMC Wideband RF Transceiver ADRV9009 based

### Technical Specifications

- Vita 57.1-2010 specification compliant
- FMC High Pin Connector (HPC)
- Analog Devices ADRV9009 RF Transceiver
- JESD024B interface up to 12288 Mbps (4x TX, 4x RX)
- LA Bus LVDA and Single-Ended
- Operates with  $V_{Adj} = 2.5V$  to  $1.8V$
- ADRV9009 ADI eval board HW/SW compatible
- Dual Transmitters (Tx)
- Dual Receivers (Rx)
- Observation Receiver (ORx) with two input
- RF Ext LO Input/Output. RX Ext LO Input/Output
- Dual FPGA GPIO Input/Output 3.3V to/from FMC
- Reference Clock Input
- RF Coverage 75MHz to 6.0 GHz. Tx Synthesis Bandwidth (BW) to 450MHz. Rx Bandwidth: 7MHz to 200MHz
- Industrial Air Cooled and Conduction Cooled versions

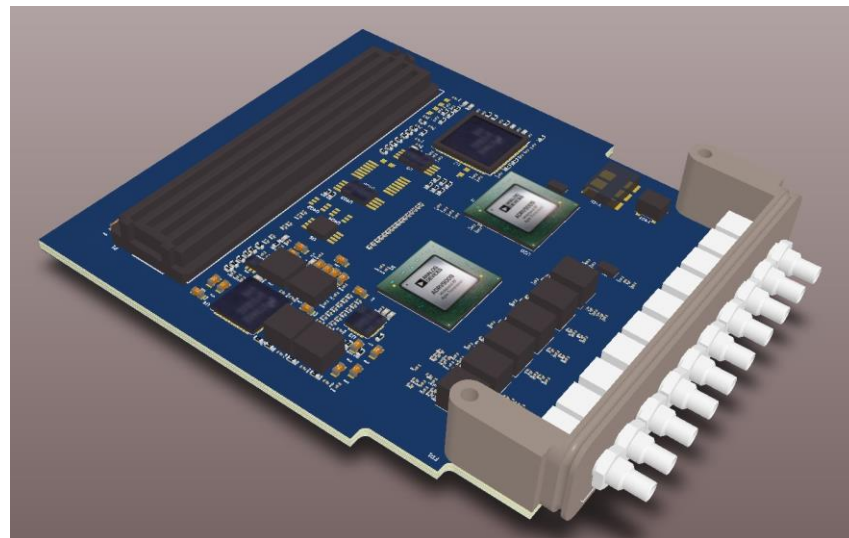


# FMC-ZU2RF-B-W (Q1 2019)

## FMC Dual Wideband RF Transceiver ADRV9009 based

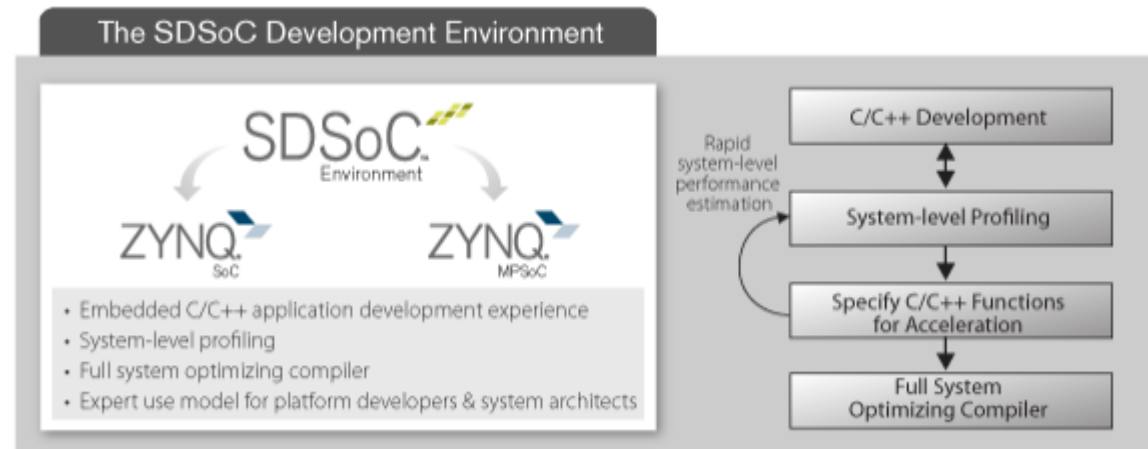
### Technical Specifications

- Vita 57.1-2010 specification compliant
- Dual Analog Devices ADRV9009 RF Transceivers
- FMC High Pin Connector (HPC)
- JESD024B interface up to 12288 Mbps (8x TX, 8x RX)
- LA Bus LVDA and Single-Ended
- Operates with  $V_{Adj} = 2.5V$  to  $1.8V$
- ADRV9009 (single chip) ADI eval board HW/SW compatible
- Quad Transmitters (Tx)
- Quad Receivers (Rx)
- Four TX and Four RX coherent channels synchronized in frequency and phase
- RF Ext LO Input/Output. RX Ext LO Input/Output
- Reference Clock Input
- RF Coverage 75MHz to 6.0 GHz. Tx Synthesis Bandwidth (BW) to 450MHz. Rx Bandwidth: 7MHz to 200MHz
- Industrial Air Cooled and Conduction Cooled versions



# Xilinx Development Tools Support

**Xilinx SDSoC:** Familiar Embedded C/C++ Application Development Experience for SoCs and MPSoCs



**Xilinx Vivado HLx:** Accelerating High Level Design



# Third Party Development Tools Support

Platform development environment support includes Industry Standard :

- Linux Industrial I/O Applications
- MATLAB
- SIMULINK
- GNU RADIO
- Streaming Interface for custom C, C++ , PYTHON and C#

Analog Devices (Open Source) HDL reference design and driver to allow optimized Time To Market

# Software BSP Support

## **LINUX**

Xilinx PetaLinux (from PanaTeQ and free of charge)

Yocto Linux

Mentor Embedded Linux

Windriver Linux

## **RTOS & Bare Metal**

Xilinx SDK

FreeRTOS

Wind River VxWorks 7

Syngo PikeOS

QNX

# Product Codification for Ordering

## VPX3-ZU1-SDR- a - b c d – rl

a	RF Tranceiver	b	Device Size	ARM A53 Cores	GPU Mali	Logic Cells	DSP Slices	Memory
A	1x AD9371	A	XCZU6EG	4	Yes	469K	1973	25.1 Mb
B	1x AD9375	B	XCZU9EG	4	Yes	600K	2530	32 Mb
C	1x ADRV9009	C	XCZU15EG	4	Yes	747K	3528	57.7 Mb
D	2x ADRV9009	D	XCZU6CG	2	No	469K	1973	25.1 Mb
		E	XCZU9CG	2	No	600K	2530	32 Mb

c	Speed Grade	d	PS / PL DDR4	rl	Ruggedization Level	Vita 47
1	Slowest	M	4GB / 1GB	AS	Air Standard	EAC4
2	Mid	P	8GB / 2GB	AR	Air Rugged	EAC6
3	Fastest			CC	Conduction Cooled	ECC3
				CR	Conduction Rugged	ECC4

Thank You

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